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PATENT

Attorney Docket No.: SONY-16000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Application of:

Richard A. Bardini et al.

Serial No.: 10/091,638

Filed: March 5, 2002

For: **METHOD OF FLOW CONTROL
FOR DATA TRANSPORTED
USING ISOCHRONOUS PACKETS
OVER AN IEEE 1394-2000 SERIAL
BUS NETWORK**

) Group Art Unit: 2616

) Examiner: Patel, Chandrahas B

) **TRANSMITTAL LETTER**

) 162 N. Wolfe Road
) Sunnyvale, CA 94086
) (408) 530-9700

) Customer No. 28960

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed please find a Supplemental Information Disclosure Statement, and Form PTO-1449, including copies of the two (2) references, for filing in the U.S. Patent and Trademark Office. Also enclosed is a check in the amount of \$180.00, for the filing fee.

The Commissioner is hereby authorized to charge any additional fee or credit overpayment to our Deposit Account No. 08-1275. **An originally executed duplicate of this transmittal is enclosed for this purpose.**

Respectfully submitted,
HAVERSTOCK & OWENS LLP

Dated: October 23, 2007

By: Jonathan O. Owens
Jonathan O. Owens
Reg. No.: 37,902

Attorneys for Applicants

CERTIFICATE OF MAILING (37 CFR § 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450

HAVERSTOCK & OWENS LLP.

Date: 10/23/07 By: [Signature]



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) **SUPPLEMENTAL INFORMATION**
) **DISCLOSURE STATEMENT**

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Sir:

The citations listed below, may be material to the examination of the above-identified application, and is therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. §§ 1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

Applicants have become aware of the following printed publications which may be material to the examination of this application:

- "Digital interface for consumer audio/video equipment - Part 1: General" Final Draft International Standard, Project number 61883-1.
- "Hitachi Ltd., Intel Corporation, Matsushita Electric Industrial, Co. Ltd., Sony Corporation and Toshiba Corporation, "The Founders"."5C Digital Transmission content Protection White paper", Revision 1.0, July 14, 1998, pgs. 1-13.

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This Supplemental Information Disclosure Statement under 37 C.F.R. §§ 1.56 and 1.97 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that anyone or more of these citations constitutes prior art.

Respectfully submitted,
HAVERSTOCK & OWENS LLP

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Date: 10/23/07 By: 